

# 3 1746

Patent  
259/079  
P01-0015

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KENNY, Michael et al.

Serial No.: 09/925,884

Filed: August 6, 2001

For: **Process And Apparatus For Treating A  
Workpiece Such As A Semiconductor Wafer**



) Group Art Unit: 1746

) Examiner: Not yet assigned

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In compliance with the Applicants' duty under 37 CFR 1.97-98, the following information is brought to the attention of the Examiner. The items are listed on the attached form PTO-1449 and copies are enclosed for the convenience of the Examiner.

The item identified in this Supplemental Information Disclosure Statement may or may not be "material" pursuant to 37 CFR 1.56 and the submission thereof by Applicants shall not be construed as an admission that any such patent, publication or other information referred to therein is

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(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

April 8, 2002  
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material or considered to be material (37 CFR 1.97(h)), or even qualifies as "prior art" under 35 U.S.C. § 102 with respect to this invention unless specifically designated by Applicants as such.

The filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information, as defined in 37 CFR 1.56, exists.

Applicant believes that no fee should be required for consideration of this Information Disclosure Statement. Should Applicant be in error, however, please charge any fee required for the consideration of this paper to Deposit Account No. 12-2475.

Respectfully submitted,

LYON & LYON LLP

Dated: April 8, 2002

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